

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claim 1 (currently amended) A UWB transmitter comprising:

an echo detector for detecting ~~[[the]]~~ a polarity of an echo at ~~[[the]]~~ a position of ~~[[the]]~~ a second chip period that is shorter than ~~[[the]]~~ a first chip period, which is ~~[[the]]~~ an original chip period in advance; and

a pulse generator for generating a pulse at the second chip period when generating a pulse of ~~[[the]]~~ a same polarity as that of ~~[[the]]~~ an immediately preceding pulse and generating a pulse at the first chip period when generating a pulse of ~~[[the]]~~ an inverse polarity to that of the immediately preceding pulse ~~where~~ when the echo detected by said echo detector has the same polarity, and for generating a pulse at the first chip period when generating a pulse of the same polarity as that of the immediately preceding pulse and generating a pulse at the second chip period when generating a pulse of the inverse polarity to that of the immediately preceding pulse ~~where~~ when the echo detected by said echo detector has the inverse polarity,

wherein said echo detector operates as a correlator for decoding codes in a receiver.

Claim 2 (original) The UWB transmitter as set forth in claim 1, wherein the second chip period is $1/2$ of the first chip period.

Claim 3 (canceled).

Claim 4 (original) A UWB receiver comprising: an echo detector for detecting the polarity of an echo at the position of the second chip period that is shorter than the first chip period which is the original chip period in advance;

a pulse generator for generating a pulse at the second chip period when generating a pulse of the same polarity as that of the immediately preceding pulse and generating a pulse at the first chip period when generating a pulse of the inverse polarity to that of the immediately preceding pulse where the echo detected by said echo detector has the same polarity, and for generating a pulse at the first chip period when generating a pulse of the same polarity as that of the immediately preceding pulse and generating a pulse at the second chip period when generating a pulse of the inverse polarity to that of the immediately preceding pulse where the echo detected by said echo detector has the inverse polarity; and

a correlator for correlating a pulse train from said pulse generator and a received signal.

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Claim 5 (original) The UWB receiver as set forth in claim 4, wherein the second chip period is $1/2$ of the first chip period.

Claim 6 (canceled).